

What is claimed is:

1. An LCD apparatus comprising:

an LCD panel to receive an image data externally provided and display an image;

5 a data driver to output the image data to the LCD panel;

a gate driver to output a gate driving signal to the LCD panel; and

a timing controller to provide a first control signal to the gate driver so as to control an output of the gate driving signal and to provide a second control signal to the data driver via a signal line formed on the LCD panel so as to control an output of the image data.

10 2. The LCD apparatus of claim 1, wherein the signal line is formed on an area adjacent to the data driver.

3. The LCD apparatus of claim 2, further comprising a plurality of signal transmission members electrically connecting the data driver with the LCD panel,

15 wherein the signal line receives the second control signal from the timing controller via one of the signal transmission members.

4. The LCD apparatus of claim 3, wherein the LCD panel comprises:

20 a plurality of gate lines to receive the gate driving signal via the gate driver, the gate lines disposed on the LCD panel, extended in a first direction and arranged in a second direction substantially perpendicular to the first direction; and

a plurality of data lines to receive the image data via the data driver, the data lines disposed on the LCD panel, extended in the second direction and arranged in the first direction.

5. The LCD apparatus of claim 4, wherein the signal line is extended in the first direction and is substantially parallel to the gate lines.

5 6. The LCD apparatus of claim 4, wherein the LCD panel comprises a plurality of pixel areas defined by the gate and data lines, and the gate driving signal is provided to a corresponding pixel area at a same time as that of the image data provided to the corresponding pixel area.

10 7. An LCD apparatus comprising:
an LCD panel to receive an image data and display an image;
a data driver to output the image data to the LCD panel;
a gate driver to output a gate driving signal to the LCD panel;
a timing controller to provide a first control signal to the gate driver so as to control an
15 output timing of the gate driving signal and provide a second control signal to the data driver
so as to control an output timing of the image data;
a plurality of signal transmission members to electrically connect the data driver with
the LCD panel; and
a signal line to provide the second control signal to the data driver via one of the
20 signal transmission members.

8. The LCD apparatus of claim 7, wherein the LCD panel comprises:
a plurality of gate lines extended in a first direction and arranged in a second direction
substantially perpendicular to the first direction; and
25 a plurality of data lines extended in the second direction and arranged in the first

direction.

9. The LCD apparatus of claim 8, wherein the signal line is extended in the first direction and is substantially parallel to the gate lines.

5

10. The LCD apparatus of claim 9, wherein the LCD panel comprises a plurality of pixel areas defined by the gate and data lines, and the gate driving signal and the image data are substantially simultaneously provided to a corresponding pixel area.

10

11. The LCD apparatus of claim 7, wherein the signal line is formed on the LCD panel and adjacent to the data driver.

12. An LCD apparatus comprising:

an LCD panel;

15

a data driver coupled to the LCD panel;

a gate driver coupled to the LCD panel;

a timing controller coupled to the gate driver and to the data driver; and

a signal line formed on the LCD panel, the signal line electrically connecting the timing controller with the data and gate drivers.

20

13. The LCD apparatus of claim 12, wherein the signal line is formed on an area adjacent to the data driver.

14. The LCD apparatus of claim 13, further comprising a plurality of signal transmission members electrically connecting the data driver with the LCD panel,

25

wherein the signal line receives a control signal from the timing controller via one of the signal transmission members so as to control an output of a image data from the data driver.